

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,167,059 B2
APPLICATION NO. : 10/820691
DATED : January 23, 2007
INVENTOR(S) : Robert Abraham


Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 11, line 18; In Claim 1, delete "(F_out)" and insert -- (F_out) --.
Col. 12, line 7; In Claim 7, delete "In" and insert -- in --.
Col. 12, line 18; In Claim 8, delete "fix" and insert -- for --.
Col. 12, line 20; In Claim 8, delete "second,transistor" and insert -- second transistor --.
Col. 12, line 41; In Claim 11, delete "(F_mod)" and insert -- (F_mod) --.
Col. 12, line 42; In Claim 11, delete "bite." and insert -- bits. --.
Col. 13, line 17; In Claim 16, delete "stop" and insert -- step --.
Col. 14, line 21; In Claim 20, delete "(PDCS)" and insert -- (PDCs) --.

Signed and Sealed this

Sixth Day of May, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office